



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

CARL M. ELLISON, ET AL.

Application No.: 09/538,951

Filed: March 31, 2000

For: **Platform and Method for Issuing and
Certifying a Hardware-Protected
Attestation Key**

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Art Group: 2132

JUL 07 2004

Examiner: Unassigned Technology Center 2100

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure, enclosed is a copy of Information Disclosure Statement by Applicant (form PTO/SB/08), which is being submitted before the mailing of a final Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

It is hereby stated that no item of information contained in the Information Disclosure Statement was cited in a patent office in a counterpart application, and, to the knowledge of the undersigned, after making reasonable inquiry, no item of information contained in the Information Disclosure Statement was known to any individual associated with the filing or prosecution of the subject application more than three months prior to the filing of the Information Disclosure Statement.

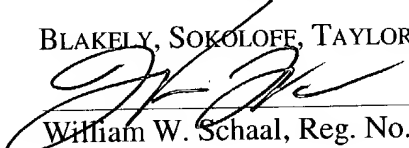
The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Applicant has concurrently submitted an electronic Information Disclosure Statement (IDS) for the above-referenced patent application. In the concurrently filed electronic IDS submission, it was requested that any requisite fees be debited from Deposit Account 02-2666. Applicant respectfully requests that only one fee be debited for these concurrently filed IDS submissions if such fees are required.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: June 29, 2004



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I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Susan McFarlane

06-29-04

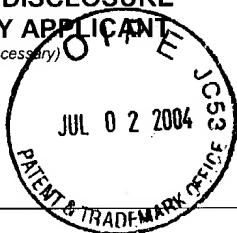
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Application Number	09/538,951
Filing Date	March 31, 2000
First Named Inventor	Ellison, Carl
Group Art Unit	2132
Examiner Name	Stulberger, Cas

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Attorney Docket No: 42P08097 Technology Center 2100

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
	EP-EP0892521	01/20/1999	Zamek, Steven			
	EP-EP1055989	11/29/2000	Proudlar, Graeme J., et al.			
	EP-EP1056014	11/29/2000	Proudlar, Graeme J., et al.			
	WO-WO0201794	01/03/2002	Ellison, Carl			
	WO-WO02060121	08/01/2002	Grawrock, David W.			
	WO-WO03058412	07/17/2003	Glew, Andrew, et al.			
	WO-WO9812620	03/26/1998	Nishiuchi, Taiki, et al.			
	WO-WO9918511	04/15/1999	Edrich, David R.			

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		COULOURIS, GEORGE, et al., "Distributed Systems, Concepts and Designs", 2nd Edition, (1994), 422-424	
		CRAWFORD, JOHN, "Architecture of the Intel 80386", <u>Proceedings of the IEEE International Conference on Computer Design: VLSI in Computers and Processors (ICCD '86)</u> , (October 6, 1986), 155-160	
		FABRY, R.S., "Capability-Based Addressing", Fabry, R.S., "Capability-Based Addressing," <u>Communications of the ACM</u> , Vol. 17, No. 7, (July 1974), 403-412	
		FRIEDER, GIDEON, "The Architecture And Operational Characteristics of the VMX Host Machine", <u>The Architecture And Operational Characteristics of the VMX Host Machine, IEEE</u> , (1982), 9-16	
		INTEL CORPORATION, "IA-64 System Abstraction Layer Specification", <u>Intel Product Specification, Order Number 245359-001</u> , (01/2000), 1-112	
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		MENEZES, ALFRED J., et al., "Handbook of Applied Cryptography", <u>CRC Press Series on Discrete Mathematics and its Applications</u> , Boca Raton, FL, XP002165287, ISBN 0849385237, (Oct. 1996), 403-405, 506-515, 570	
		NANBA, S., et al., "VM/4: ACOS-4 Virtual Machine Architecture", <u>VM/4: ACOS-4 Virtual Machine Architecture, IEEE</u> , (1985), 171-178	
		RSA SECURITY, "Hardware Authenticators", <u>www.rsasecurity.com/node.asp?id=1158</u> , 1-2	
		RSA SECURITY, "RSA SecurID Authenticators", <u>www.rsasecurity.com/products/securid/datasheets/SID_DS_0103.pdf</u> , 1-2	
		RSA SECURITY, "Software Authenticators", <u>www.rsasecurity.com/node.asp?id=1313</u> , 1-2	

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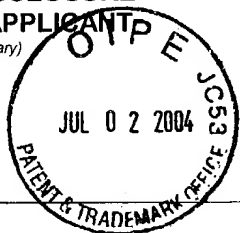
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Attorney Docket No: 42P08097 Technology Center 2100

		SCHNEIER, BRUCE , "Applied Cryptography: Protocols, Algorithm, and Source Code in C", Wiley, John & Sons, Inc., XP002939871; ISBN 0471117099,(Oct. 1995),47-52	
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